



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BALGOR

In re the Application of:

SYWE N. LEE

Serial No.: 09/433,677

Filed: November 4, 1999

For: INTERLACE OVERLAP PIXEL

DESIGN FOR HIGH SENSITIVITY

CMOS IMAGE SENSORS

Group Art Unit: 2612

Examiner: Moe, Aung Soe

RECEIVED

JAN 0 7 2004

Technology Center 2600

AMENDMENT AND RESPONSE

Hon. Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby submits this amendment in response to the Office Action mailed uly 2, 2003.

65 JUL 1937

576-00 LA

25370082.1